

## A CURRENT CONTROLLED RANDOM-ACCESS MEMORY EMPLOYING THE VORTEX HANDEDNESS

S. Bohlens<sup>1</sup>, B. Krüger<sup>1</sup>, A. Drews<sup>2</sup>, M. Bolte<sup>2</sup>,  
G. Meier<sup>2</sup>, U. Merkt<sup>2</sup>, and D. Pfannkuche<sup>1</sup>

<sup>1</sup>*I. Institut für Theoretische Physik, Universität Hamburg, Hamburg, Germany*

<sup>2</sup>*Institut für Angewandte Physik und Zentrum für Mikrostrukturforschung, Universität  
Hamburg, Hamburg, Germany*

Current-driven magnetization dynamics is currently being investigated as it is a new concept for spintronic devices and fast data storage elements. We propose a memory element based on a magnetic vortex which is operated simultaneously by a spin-polarized current and a magnetic field. Starting from our recent analytical description of the vortex motion [1-3] we developed a scheme that allows to transfer the vortex into an unambiguous binary state, defined as the product of chirality and core polarization.

In a Vortex Random Access Memory (VRAM) the information is stored in the product of two intrinsic properties of the magnetic vortex: the chirality  $c$  and the core polarization  $p$ . In a parallel current and magnetic field arrangement the amplitude of gyration depends on the sign of the product  $cp$ : An enhancement of the rotation amplitude occurs for  $cp = -1$  and a quenching for  $cp = +1$ . Therefore without the need to determine the absolute values  $p$  or  $c$ , a distinct  $cp$ -state can be obtained by field assisted current induced vortex-core switching. The writing mechanisms of the VRAM can be operated with alternating currents or short current pulses. For the reading mechanism it is necessary to determine the product  $cp$ , as neither the polarization  $p$  of a vortex nor its chirality  $c$  is unambiguously determined within the writing procedure.

The VRAM concept is non-volatile and the stability requirements for a memory device are fulfilled: the vortex state is stable against temperature and static magnetic fields as long as they remain in the millitesla regime. Foremost, the VRAM is a fast memory concept which needs no reading and no erasing before writing.

[1] M. Bolte, et al., Phys. Rev. Lett. **100**, 176601 (2008)

[2] B. Krüger, et al., Phys. Rev. B **76**, 224426 (2007)

[3] B. Krüger, et al., J. Appl. Phys. **103**, 07A501 (2008)