## LOW NOISE MIX-MODE INTEGRATED CIRCUITS FOR MICRO-SENSOR READOUT

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Experimental techniques in physics, material science, biology and medicine want to gain profit from the advantages of the VLSI technology by using a new generation of electronic readout systems based on Application Specific Integrated Circuits (ASIC). These ASICs are able to process small amplitude signals from micro-sensor array and perform conversion to digital format. In this class of integrated circuits several important problems like power limitation, low level of noise, good matching performance and crosstalk effects must be solved simultaneously. This presentation shows two ASICs which, given the original solutions implemented and their universal properties, can be used in different applications and are significant milestones in experimental techniques. The first presented ASIC is the 64-channel charge amplifier with binary readout architecture for a low energy X-ray imaging techniques. This integrated circuit connected to silicon strip detector can be used in powder diffractometry and then it reduces the measurement time by two order of magnitude. The second presented ASIC is multichannel low noise readout for extracellular neural recording, which is able to cope with extracellular neuronal recording for the systems comprising several hundreds of electrodes. This ASIC can be used to monitor the neural activity of such complicated system like retina or brain, to find the answer to principal questions: how complicated neuronal system code and process information.

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